

100 ↗

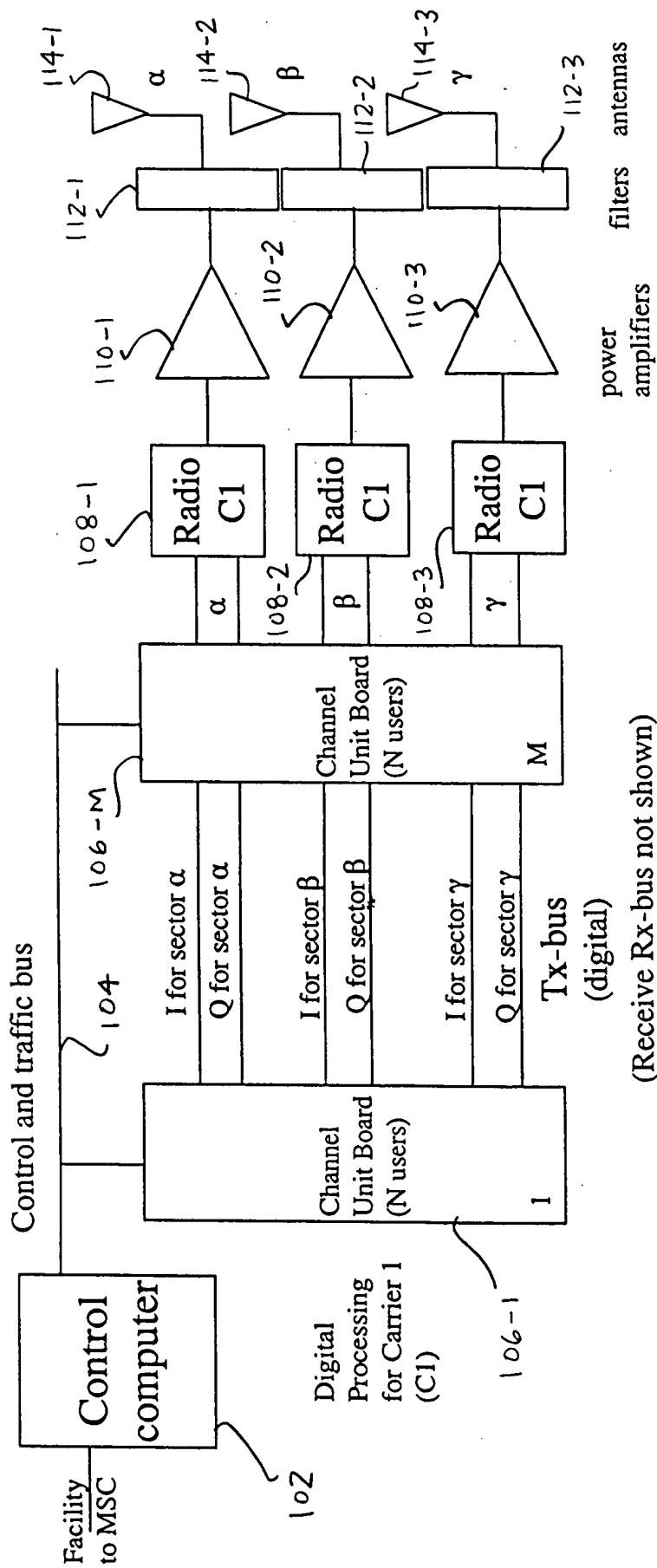


FIG. 1
(PRIOR ART)

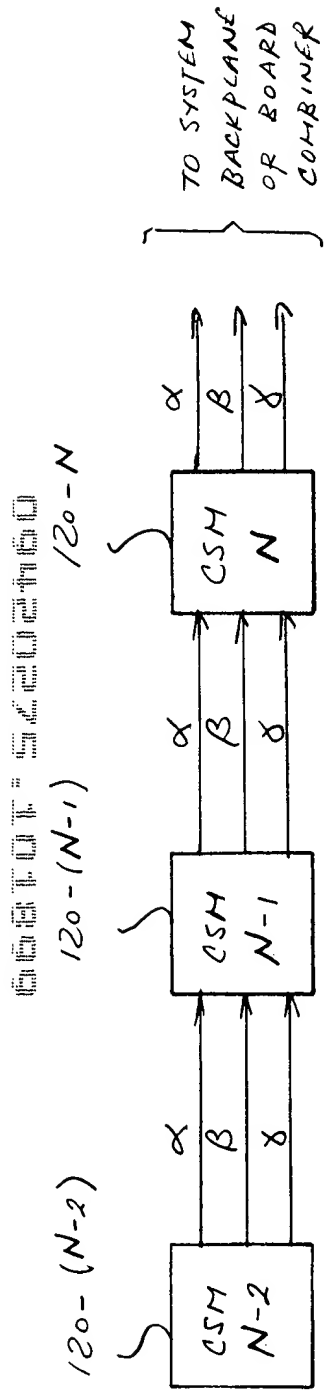


FIG. 2
(PRIOR ART)

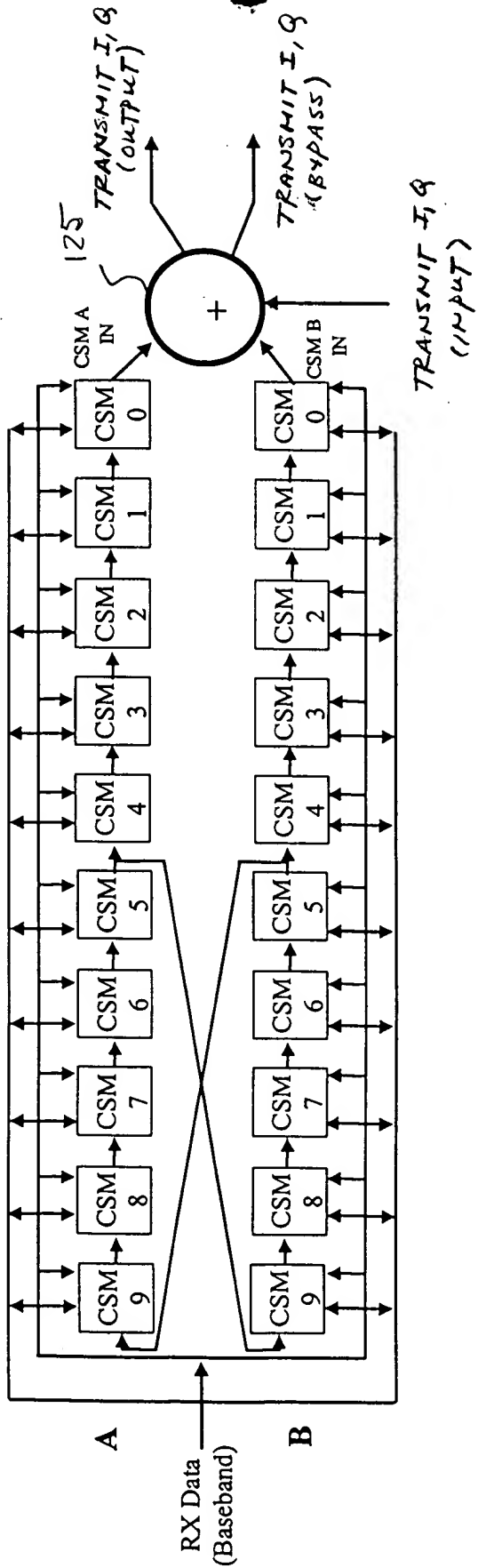


FIG. 3
(PRIOR ART)

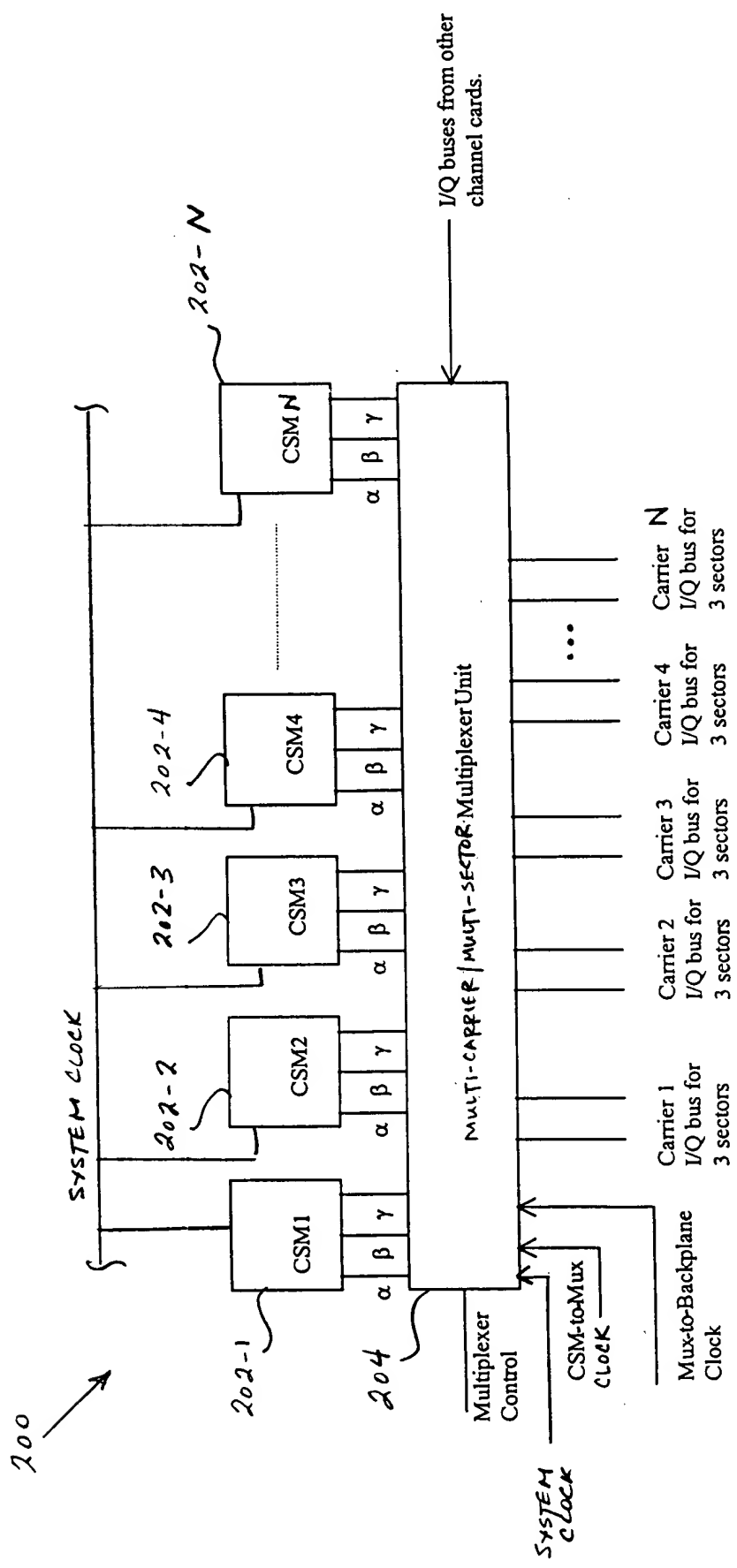


FIG. 4

SYSTEM
TRANSMIT
I/Q BUS

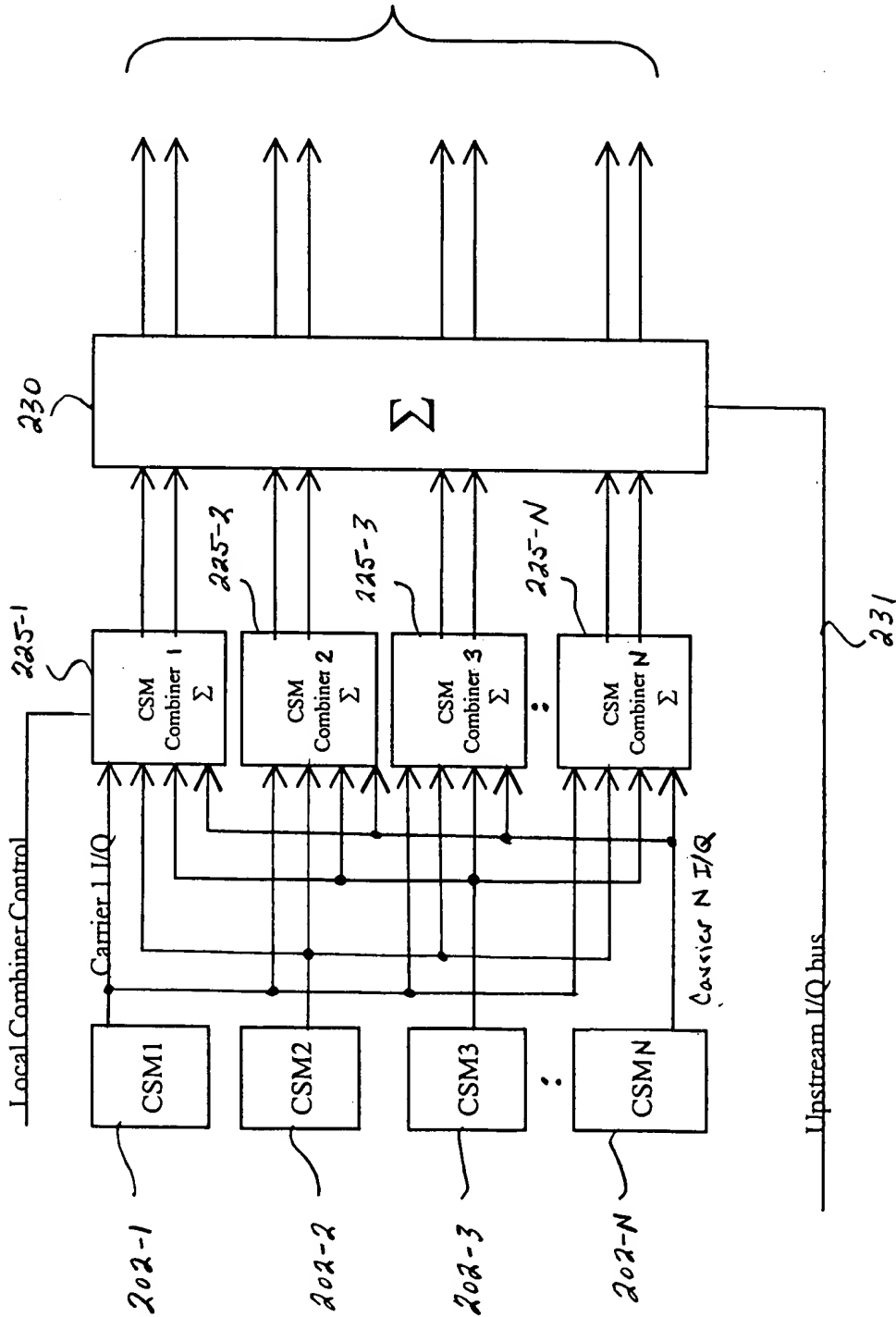


FIG. 5

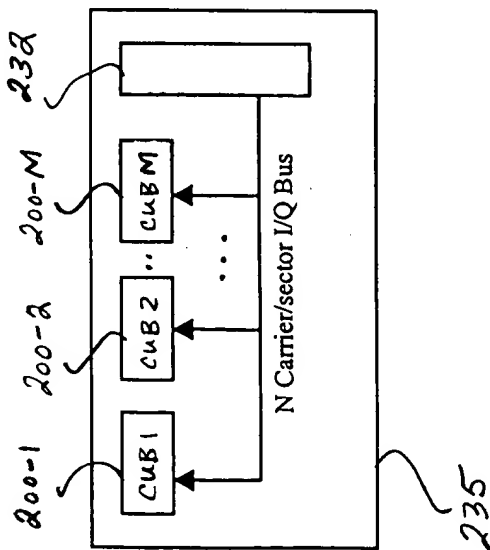
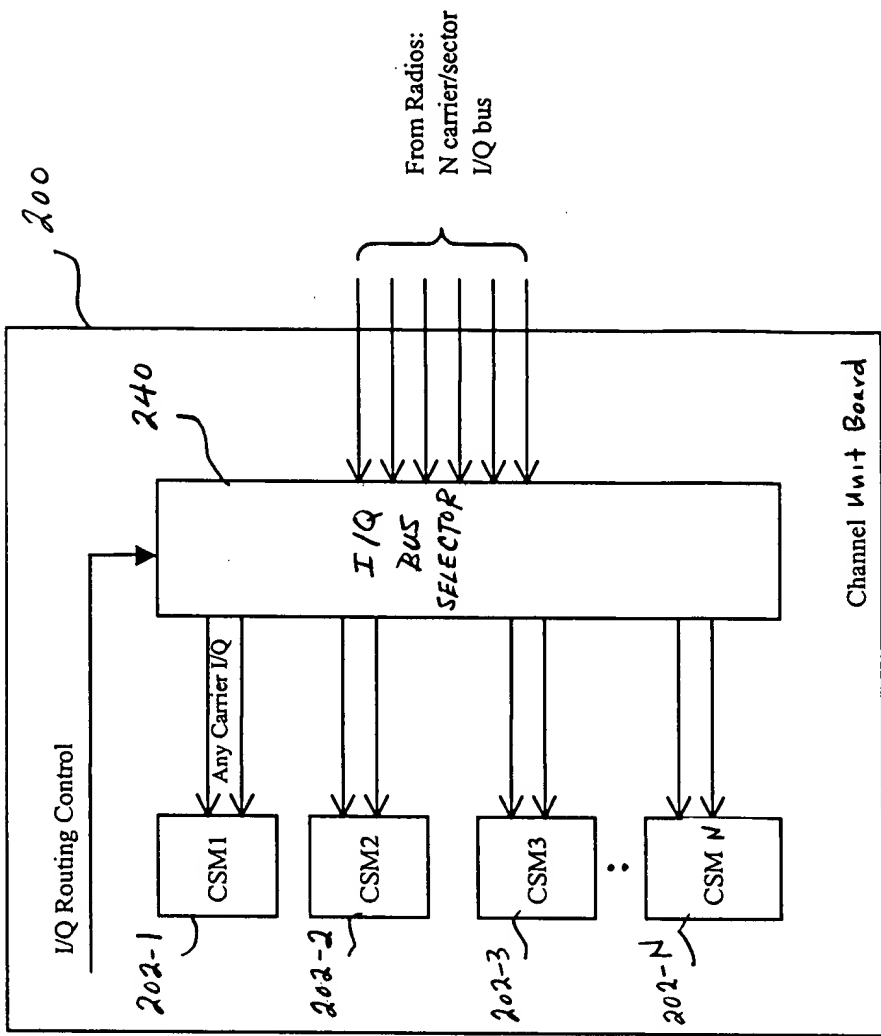


FIG. 6A

FIG. 6B